



COPY OF PAPERS  
ORIGINALLY FILED

48588  
#7  
D.M. Patel  
4-26-02  
Response  
Attorney's Docket No.: 09464-009002

UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Andrew J. Burstein et al.      Art Unit : 2838  
Serial No. : 09/892,233      Examiner : Rajnikant Patel  
Filed : June 26, 2001  
Title : TRANSISTOR PATTERN FOR VOLTAGE REGULATOR

Commissioner for Patents  
Washington, D.C. 20231

RESPONSE

In response to the action mailed December 17, 2001, please consider the following remarks.

Claims 46-78 stand rejected for obviousness-type double patenting over U.S. Patent No. 6,278,264. A terminal disclaimer is enclosed.

An Information Disclosure Statement was filed on November 1, 2001, prior to mailing of the Office Action. It appears that the documents "crossed in the mail". Applicant requests that the Examiner return an initialed copy of Form-1449 to indicate that the references have been considered.

Applicant asks that all claims be allowed.

Enclosed is a \$130 check for the terminal disclaimer fee.

CERTIFICATE OF MAILING BY FIRST CLASS MAIL

RECEIVED  
APR 15 2002  
TECHNOLOGY CENTER 2800  
I hereby certify under 37 CFR §1.8(a) that this correspondence is being deposited with the United States Postal Service as first class mail with sufficient postage on the date indicated below and is addressed to the Commissioner for Patents, Washington, D.C. 20231.

March 18, 2002  
Date of Deposit

Signature

Nikia McMillan  
Typed or Printed Name of Person Signing Certificate